

# XRD 0820 AN

## Digital X-Ray Detector



### Overview

The XRD 0820 AN is a digital X-Ray detector based on a 8" amorphous silicon sensor operating as a two-dimensional photodiode array. The XRD 0820 AN is well suited to perform digital X-Ray imaging and contains all drive and read-out electronics including an X-Ray interlock. X-Rays are converted into light using a Kodak™ Lanex Fast, Lanex Fine, Kasei DRZ Standard, DRZ Plus, or a CsI scintillator. The information is digitized in 16 bits to achieve highest dynamic range and contrast. With a pixel size of 200 μm, an image size of 1024 x 1024 pixels, and a frame rate of 7.50 Hz, the detector is designed to fulfill the requirements of a variety of Industrial and Medical Applications.

The XRD 0820 AN series detectors are connected to a PCI computer, with the data transferred via a customized parallel interface. Up to four detectors, each using its own Frame Grabber can be connected to one PC. The XRD 0820 AN, the XRD-EP Power Supply Unit and the PCI I/O board are optimized for the highest performance. The image integration time is variable between 133 ms and 5 sec in steps of 1 ms (internal timer) or can be set between 133 ms and 2 sec in eight fixed steps (free running). The XRD 0820 AN series provides the advantage of synchronization between the detector and x-ray source or manipulator by using an external trigger signal.

### Features and Benefits

- Complete Digital X-Ray Detector
- Monolithic Flat Panel
- > 1 Million Pixels
- 200 μm Pixel Pitch
- 65.536 Grey Levels
- Ultra High Sensitivity
- Live Images @ 7.50fps
- Suitable for a wide range of X-Ray energies
- Selectable Gain Setting

### Applications

- Non-Destructive Testing
- 3D CT Reconstruction
- Scientific & Medical Applications

The XRD image acquisition and demonstration software and the XRD image acquisition software library are included. The software library can be used to integrate the specific detector functions into various types of image processing software. The library supports functions for

- acquisition of a single frame or a sequence
- selection of integration times
- selection of gain setting
- selection of trigger modes
  - Free Running;
  - External Trigger Source
  - Internal Timer
  - Software Trigger
- calibration procedures to acquire offset and gain correction files
- perform online corrections for
  - Offset correction
  - Multiple Gain Correction
  - Pixel Correction

<b>Panel Specification</b>	
Scintillator screen (standard):	Lanex™Fine*/Lanex™Fast*/DRZ**-STD/DRZ**-PLUS
(optional)	CsI
Pixel number:	1024 x 1024
Active Pixel number	1000 x 1000
Pitch:	200 µm
Total area:	204.8 x 204.8 mm <sup>2</sup>
Diode Capacity	2.1 pF

<b>Electronics Specification</b>	
Charge Amplifier	16 x 128 channel ASIC
Feedback capacitance (gain)	0.25pF, 0.5 pF, 1 pF, 2 pF, 4 pF, 8 pF
ADC:	16 x 16bit A/D @ 1MSps
Integration time (minimum):	133.0 ms @ 200µm 66.2 ms @ 400µm (2x2 Binning)
Non-Linearity <sup>1</sup>	< 1 % (10 % to 90 % FSR)

<b>Detector Specification</b>	
Dynamic range <sup>1</sup>	> 80 dB
Response Non Uniformity <sup>1</sup>	±2 % (10 % to 90 % FSR)
Image lag: (standard)	< 8 % (1 <sup>st</sup> frame)
(CsI-option)	< 10 % (1 <sup>st</sup> frame)
Frame rate (max):	7.5 fps @ 200µm 15 fps @ 400µm (2x2 Binning)
Radiation energy:	40 keV – 225 keV
Detector housing:	335 x 320 x 52 mm <sup>3</sup>

For CsI Option (75 kVp, 20 mm Al filtration, 7 mm Al HVL):	
MTF (0.5 lp/mm)	80% (typical 90%)
MTF (2.0 lp/mm)	30% (typical 38%)
DQE (0.5 lp/mm)	56% (typical 58%)
DQE (2.0 lp/mm)	28% (typical 37%)

<b>Requirements</b>	
Power Supply	XRD-EP (95510254H)
Frame Grabber:	XRD-FG (95510214H)
PC-Requirements***:	CPU > 3 GHz
	RAM > 1 GB
	PCI Bus
	Windows™2000, XP

<sup>1</sup> At 1pF Gain and 7.5 frames per second  
 \*Lanex™ is a registered trademark of Eastman Kodak Company; \*\*DRZ is a trademark of Kasei Optonix  
 \*\*\*Windows™2000 and Windows™ XP are registered trademarks of Microsoft Cooperation

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